Daniel Antonio Martínez-Sánchez

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Summary

Four year (out of four) Computer Science undergraduate at the University of Murcia. Right now I'm also an intern in the computer architecture department where I do research and also manage a couple of HPC clusters consisting of 14 servers (Xeon, AMD EPYC, GPUs). Currently I'm developing my undergraduate thesis about MLIR optimizations using optimized libraries and AI auto-tuning for performance improvements.

Education

University of Murcia *Murcia, ES*
Degree in Computer Science *Sept 2022 - current*

- Member of the GACOP (Parallel Computing and Computer Architecture Group) research group
- Intern with advisor José Manuel García Carrasco
- GPA of 9/10, 14 honours

Work Experience

Compiler Engineer - Intern *Remote*
Huawei Cambridge *July 2025 - September 2025*

- Enabling SVE/SME instructions into triton-cpu
- Code lifting from triton IR to **MLIR**
- Fixing MLIR upstream and downstream bugs
- Created new optimization passes for lifting on triton-shared

University of Murcia *Murcia, ES*
HPC cluster admin for GACOP and Bioinformatics *Jan 2023 - Current*

- Administration of **Linux** HPC servers used for research
- Usage of **Docker, Ansible, Google Cloud and Slurm**
- Solving user's problems

Projects

CUDA programming *Murcia, Barcelona, ES*
Barcelona Supercomputing Centre and University of Murcia *June 2024 - August 2024*

- Introduction to **CUDA** via official web courses at NVIDIA
- CUDA usage in C++ and Python
- Summer School at Barcelona Supercomputing Centre

Computer Architecture Intern *Murcia, ES*
University of Murcia *January 2023 - Current*

- Administrator of various HPC clusters (2023 - Current)
- Application of a convolutional model for cervics cancer (2023)
- Introduction to MLIR/LLVM and ML compilers (2024)

Skills

Technical Skills   **MLIR**, Computer Architecture, Linux, Docker, Ansible, C, CUDA, Google Cloud
Soft Skills   Efficiency, Teamwork, **Problem solving**, Communication

Languages

English   Cambridge B2
Spanish   Mother tongue